

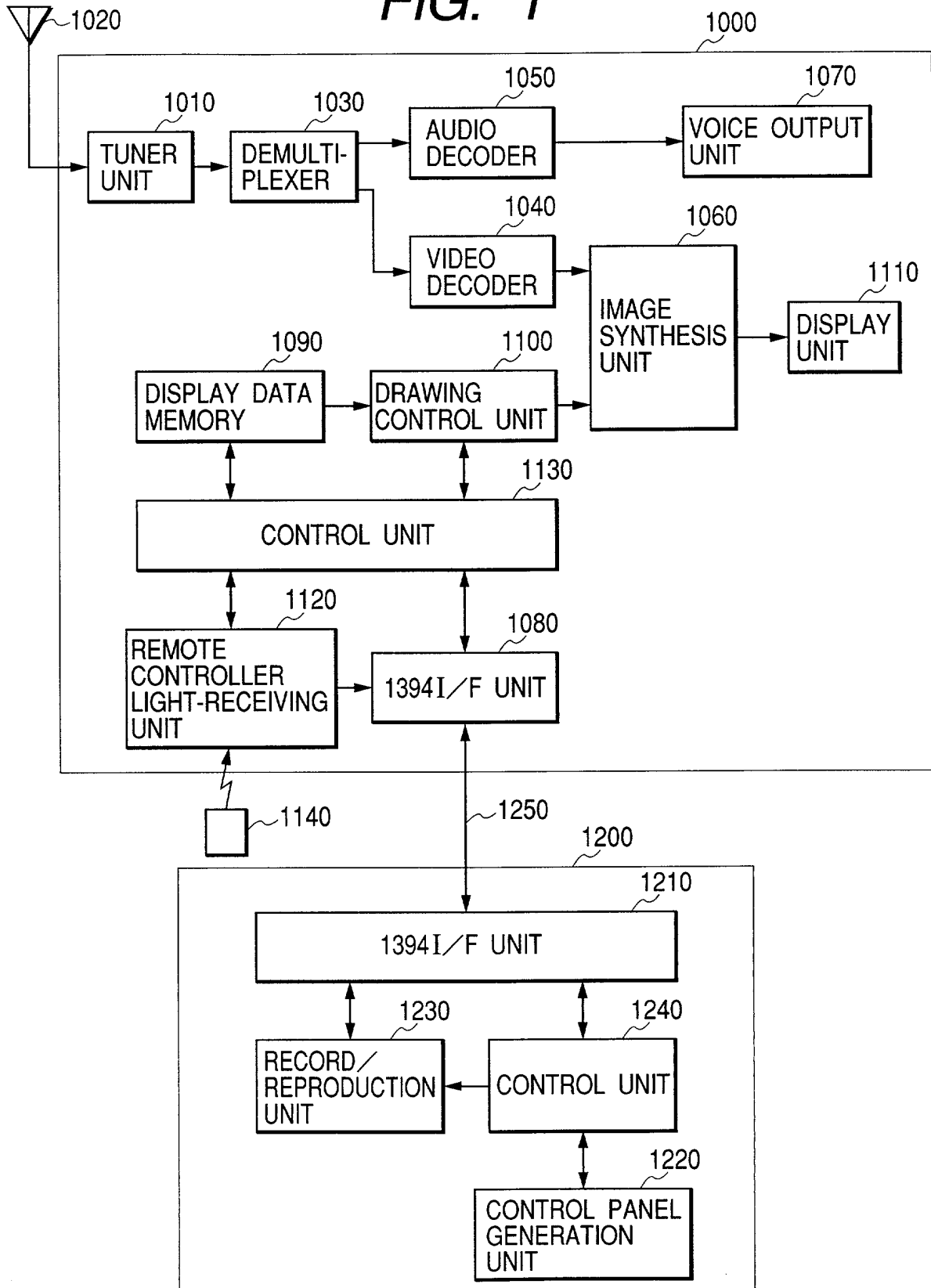
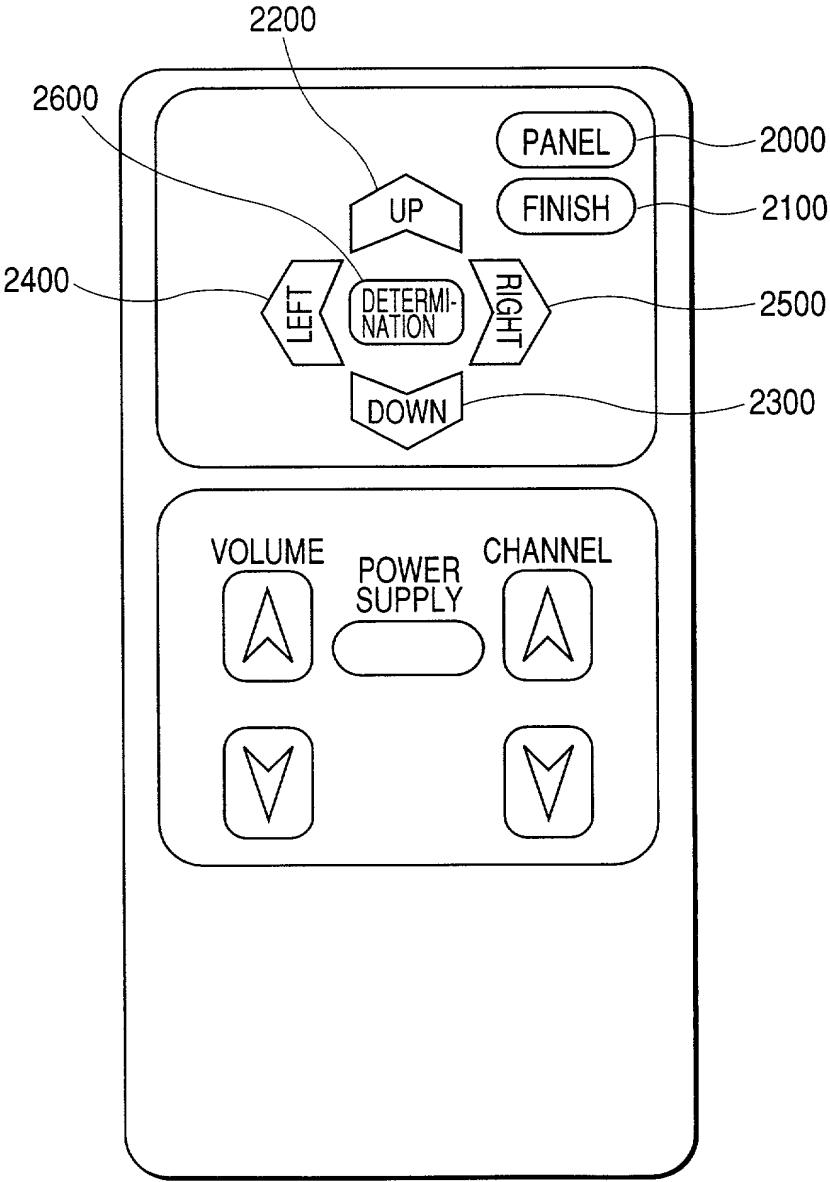
**FIG. 1**

FIG. 2



**FIG. 3**

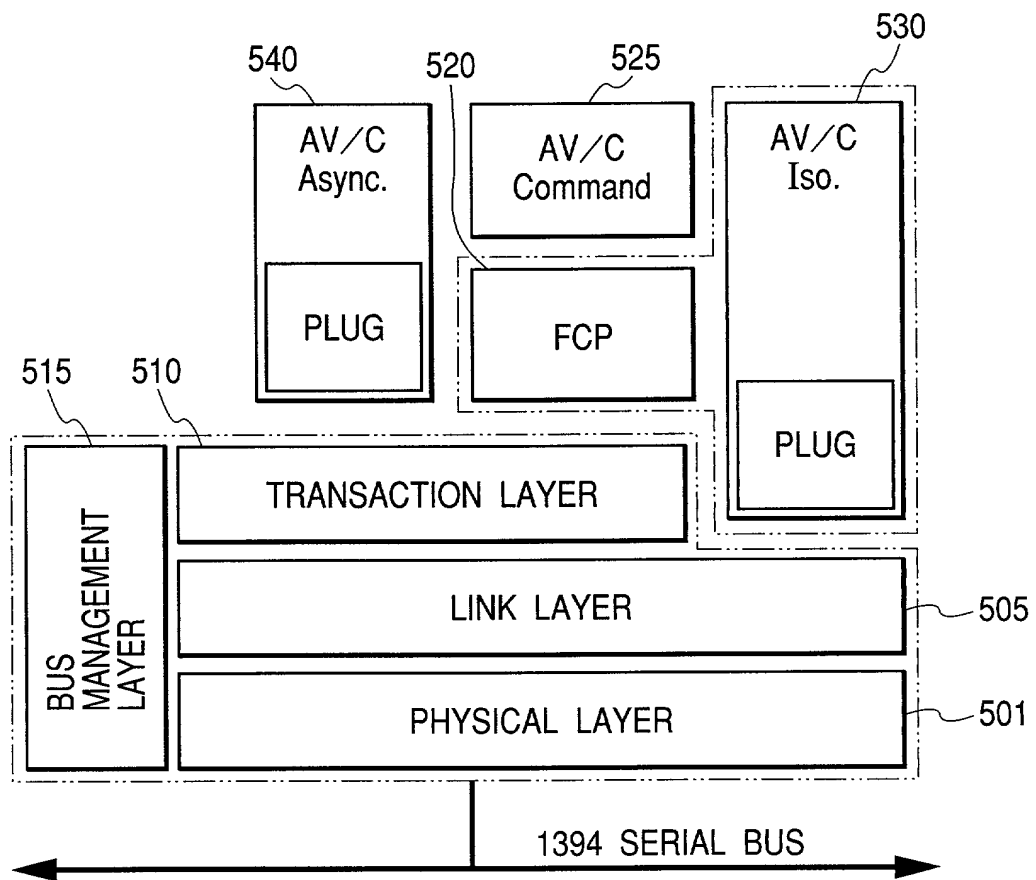
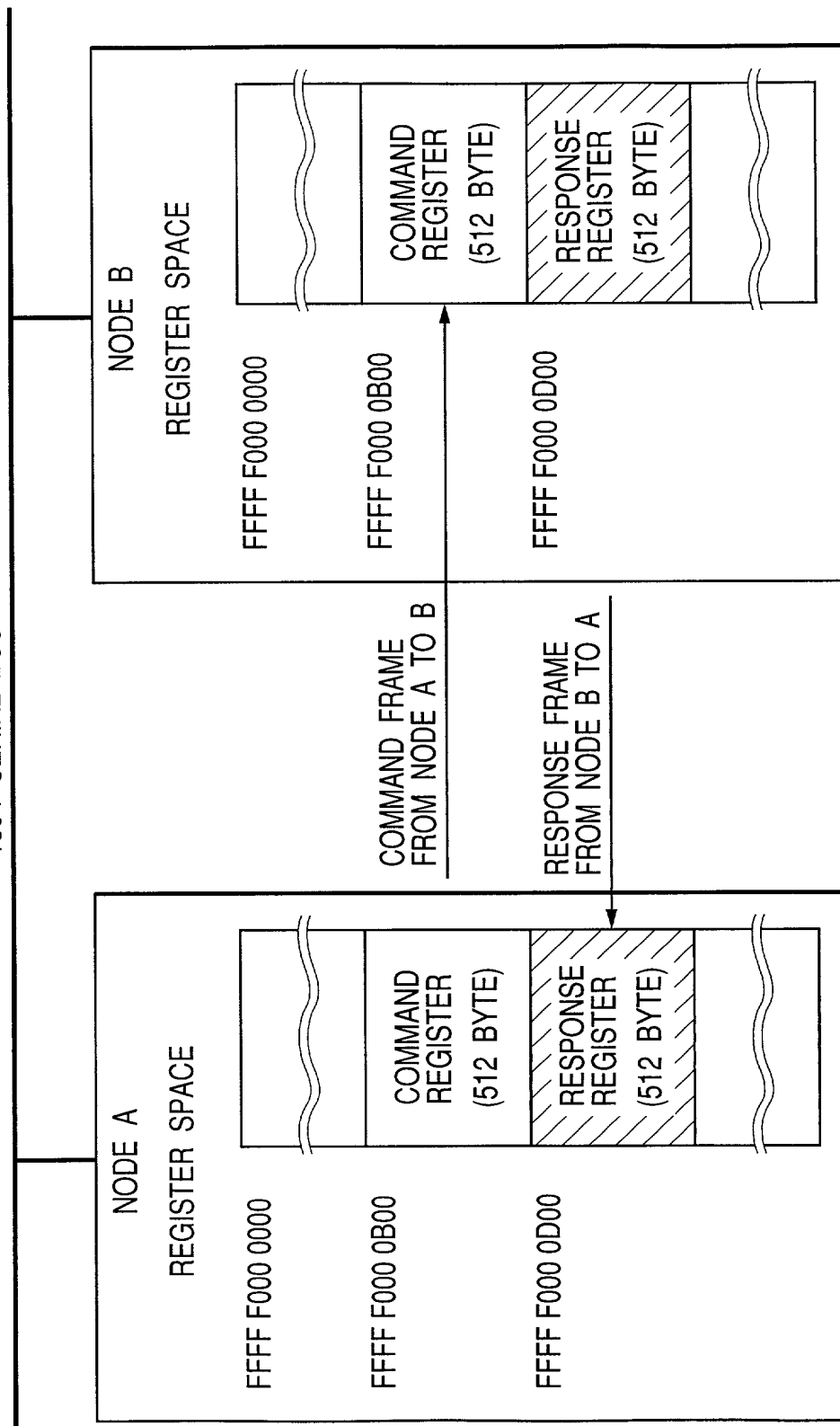


FIG. 4

1394 SERIAL BUS



**FIG. 5A**

Destination_ID	TI	Rt	tcode	Pri				
Source_ID								
Destination_offset								
Quadlet_data(FCP frame)								
Header_CRC								

Quadlet\_write

**FIG. 5B**

Destination_ID	TI	Rt	tcode	Pri
Source_ID				
Destination_offset				
data_Length	Extended_tcode			
Header_CRC				
Block_data(FCP frame)				
"0"padding(if necessary)				
Data_CRC				

block\_write

FIG. 6

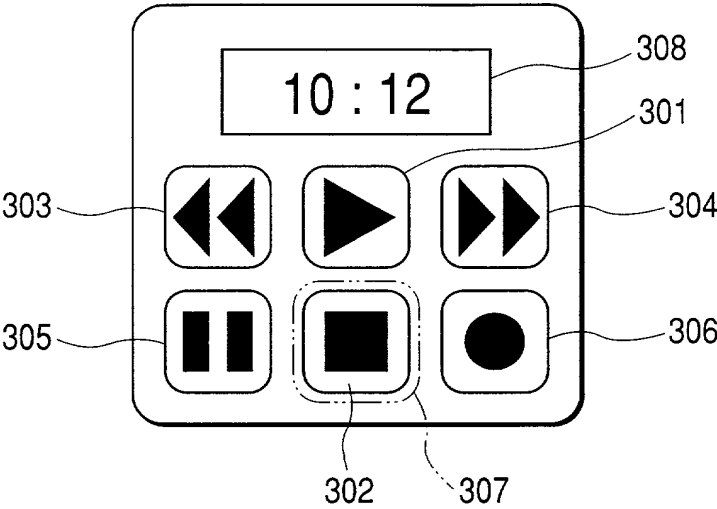
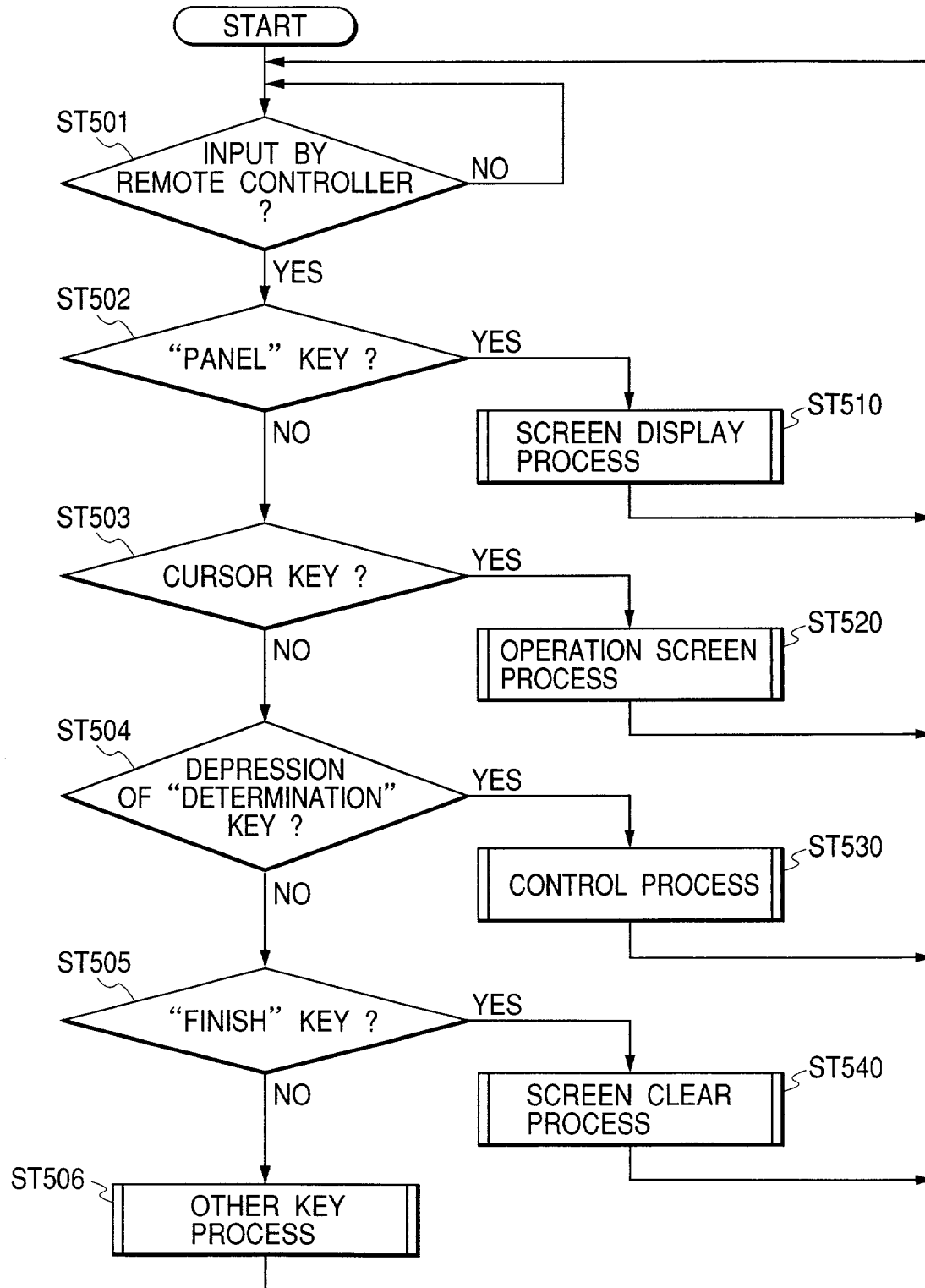


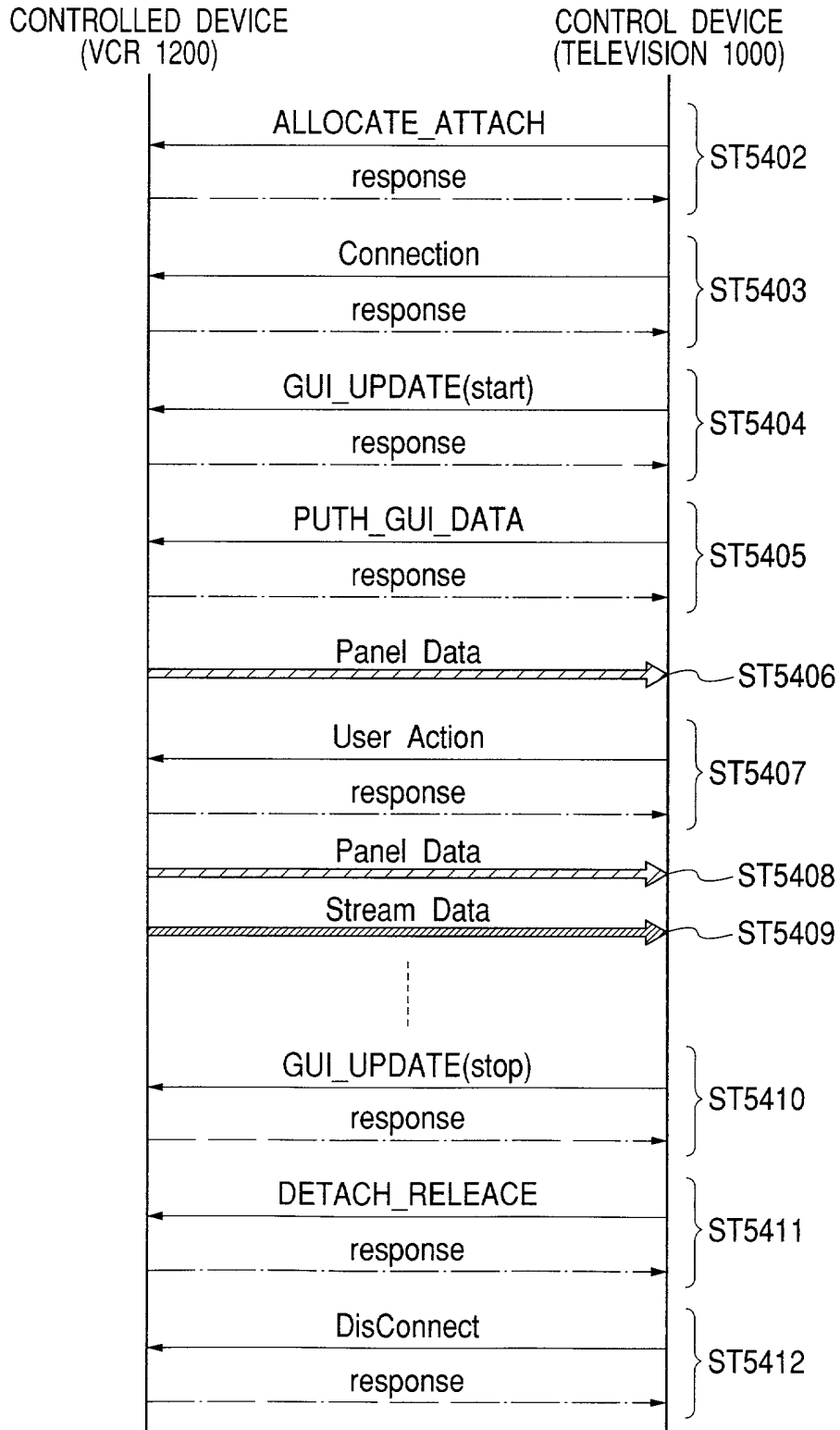
FIG. 7

BUTTON ID	BUTTON ID
301	REPRODUCTION BUTTON 301
302	STOP BUTTON 302
303	REWIND BUTTON 303
304	FORWARD BUTTON 304
305	TEMPORARY STOP BUTTON 305
306	RECORD BUTTON 306

FIG. 8

HISTORY ID	CONTENTS 1	CONTENTS 2
503	301	303
504	301	304

**FIG. 9**

**FIG. 10**



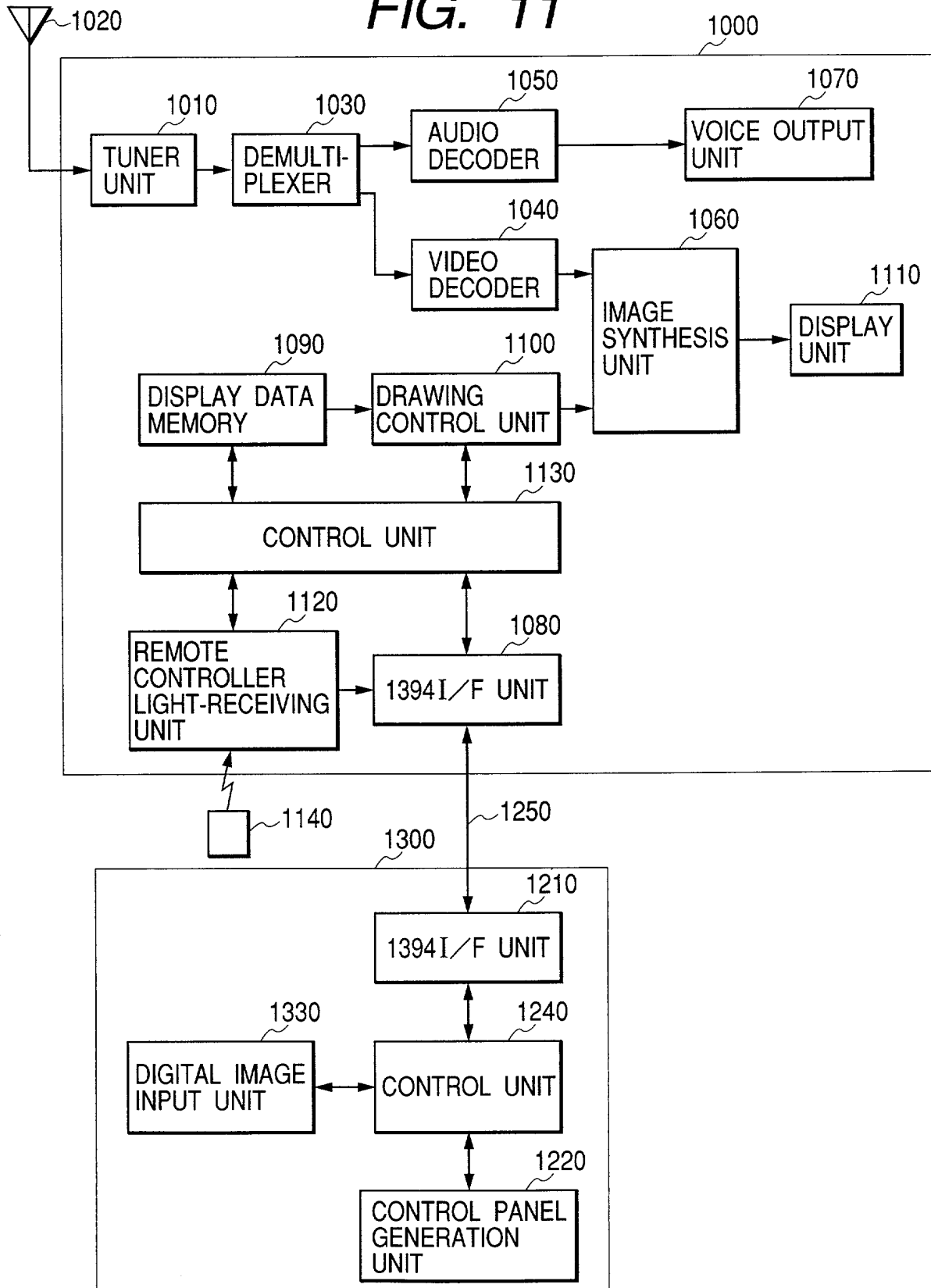
**FIG. 11**

FIG. 12

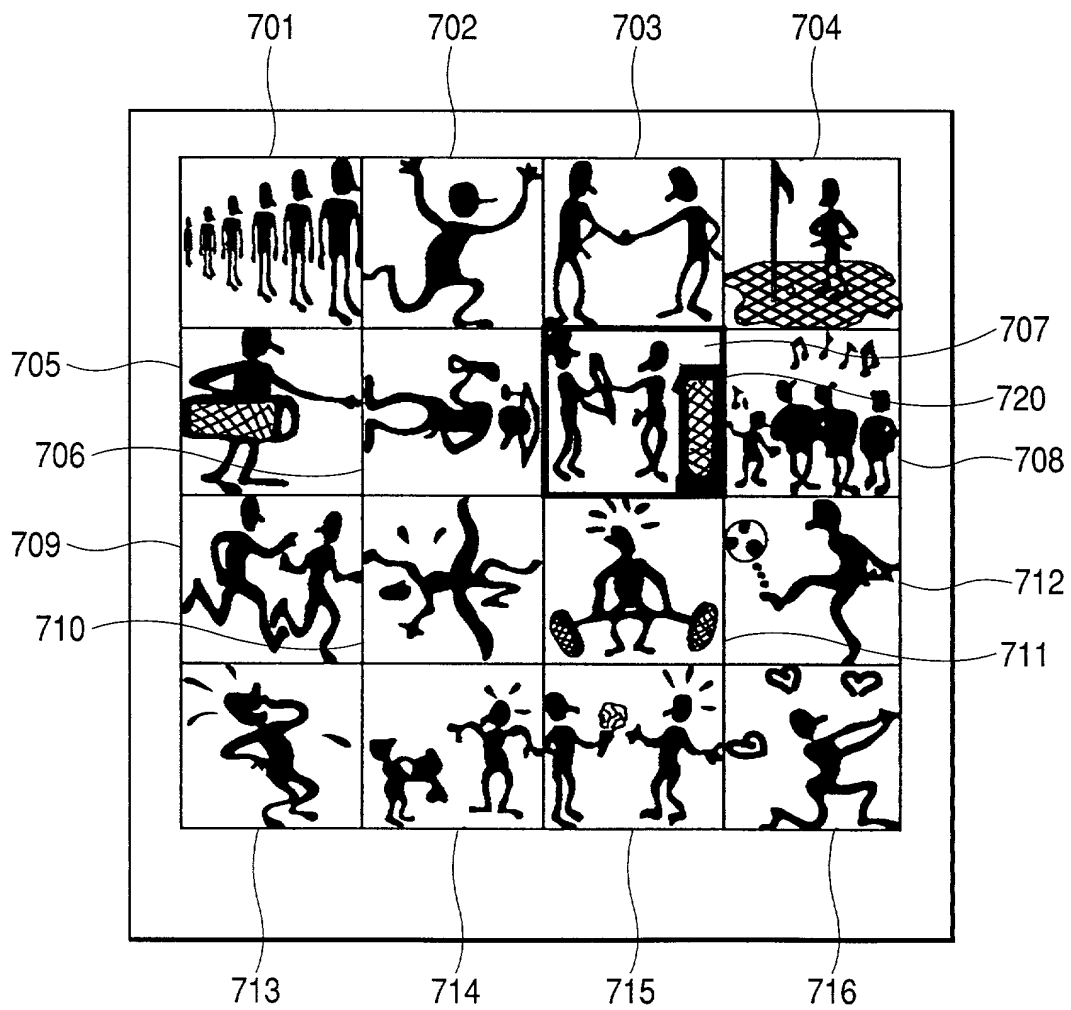
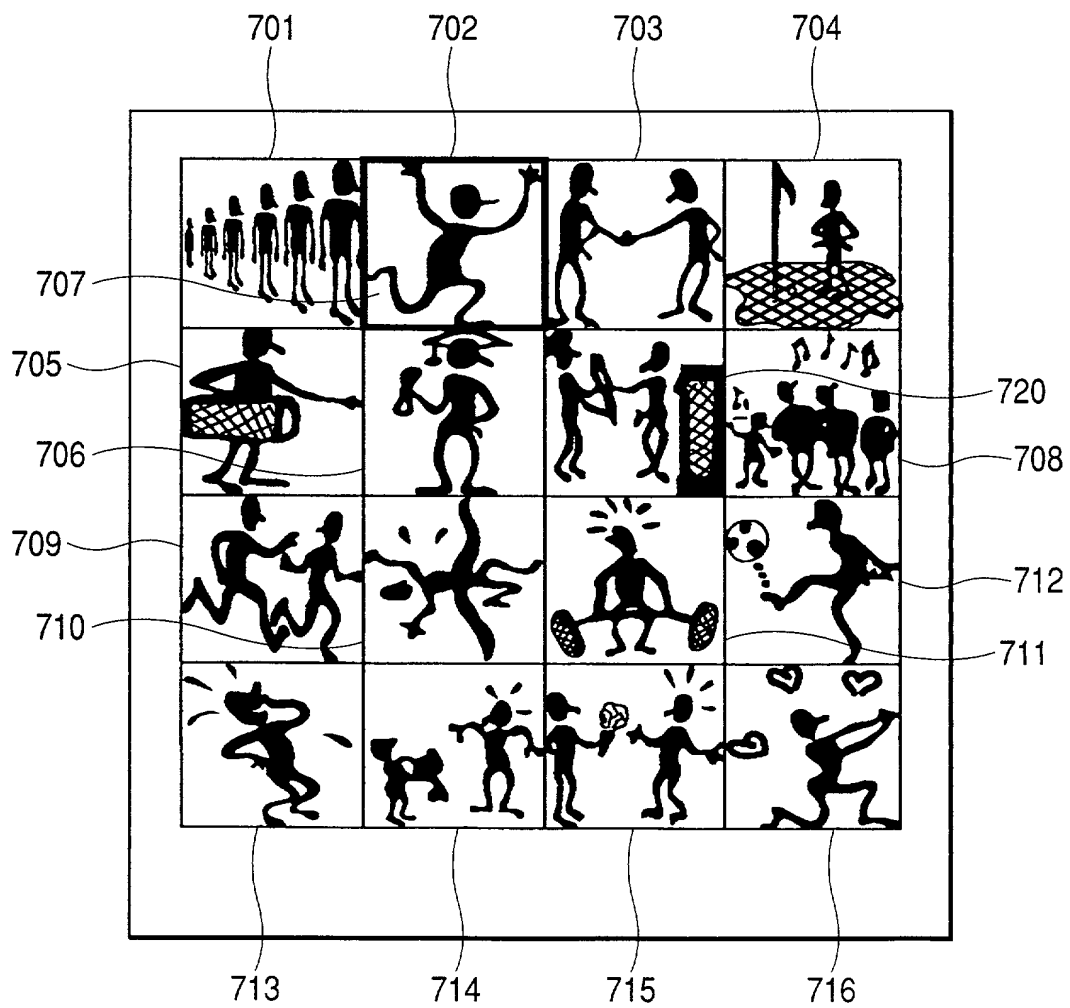


FIG. 13



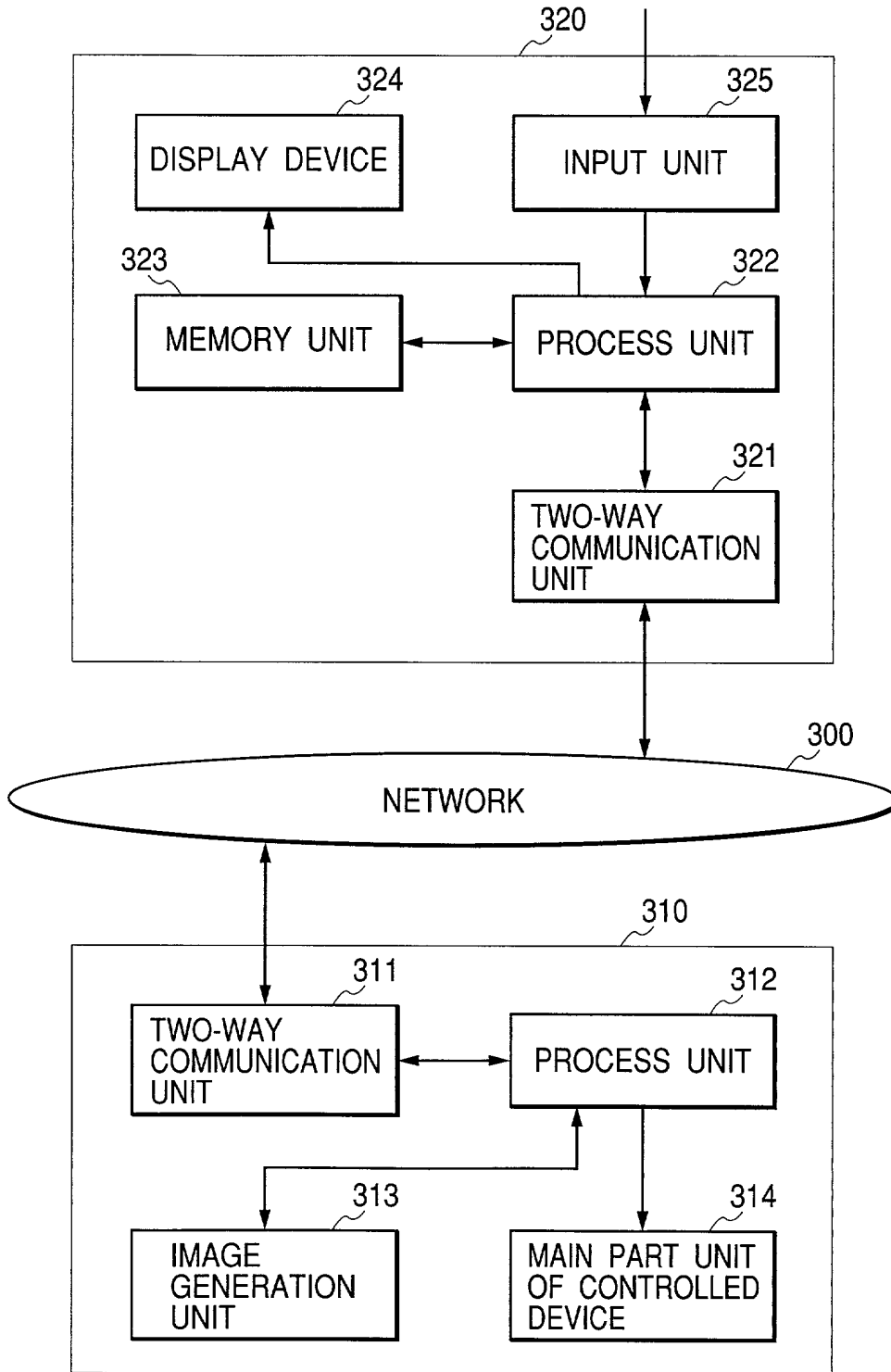
**FIG. 14**

FIG. 15

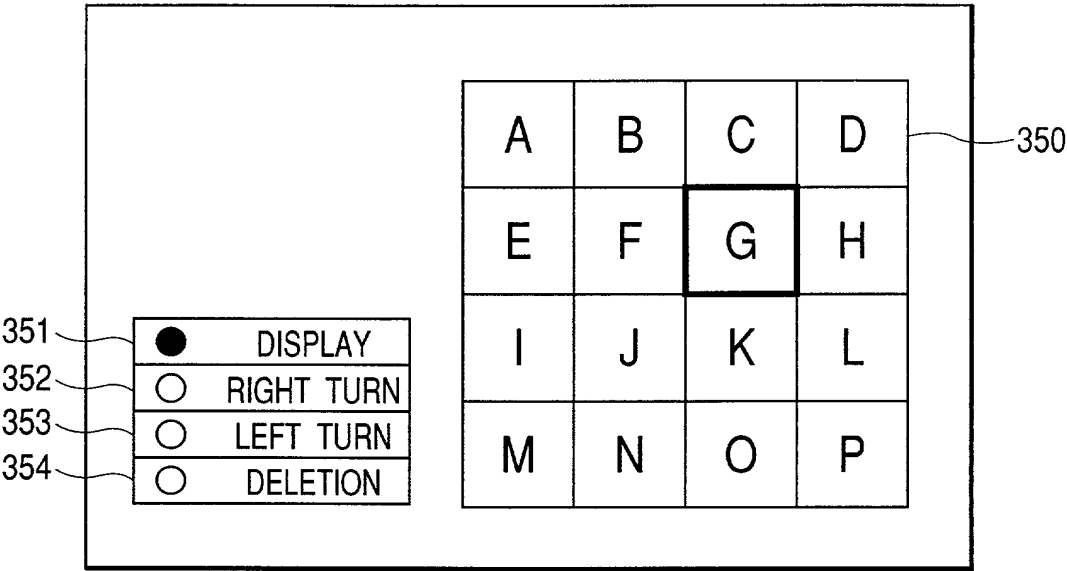


FIG. 16

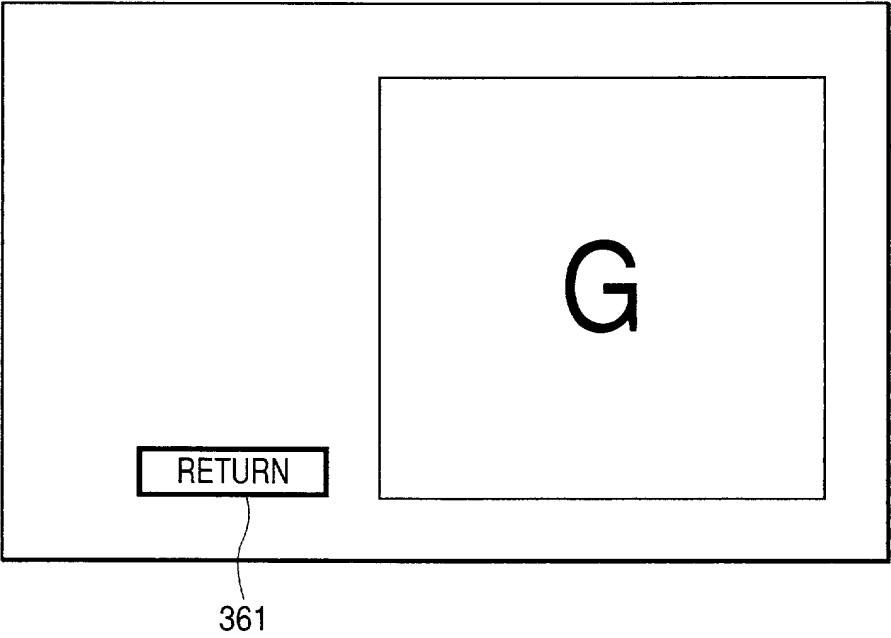


FIG. 17

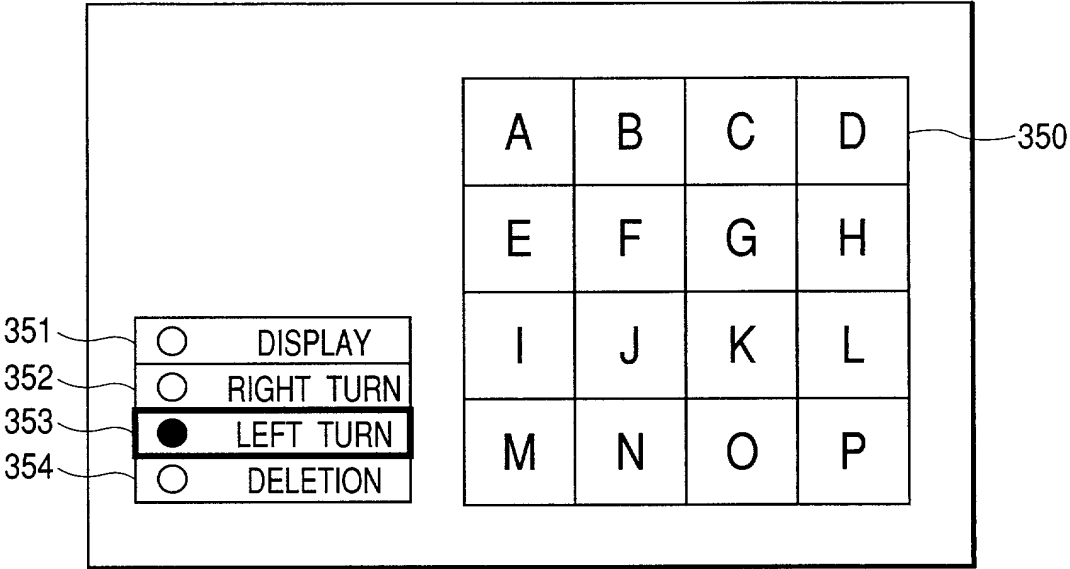


FIG. 18

